PLEASE NOTE THAT THE LAST DATE FOR SUBMISSION HAS BEEN EXTENDED UPTO 5:00pm, January 6, 2012)

Notice Inviting Quotations

Quotations are invited for programmable analog signal processing boards/kits suited to signal conditioning, filtering, gain, rectification, summing, subtracting, multiplying, etc; able to accommodate nonlinear functions such as sensor response linearization and arbitrary waveform synthesis. The programmable analog systems must be on integrated circuits. Each device/board/kit must contain at least 4 configurable analog blocks, with programmable interconnect resources and analog input/output cells with active elements. On-chip temperature compensated band-gap based reference voltage levels should be available. Look up tables for facilitating waveform synthesis and non-linear functions should be provided. Configuration data should be stored in an on-chip SRAM configuration memory. An SPI like interface should be provided for simple serial load of configuration data from a microprocessor or DSP. On-chip analog blocks should include differential amplifiers and chopper stabilized amplifiers.

Please quote costs on FOB basis. The freight payment and customs clearance will be done by IIT Delhi through its Authorised Customs handling Agent and Freight Forwarder (http://www.parakshift.com/contact.html) M/s PRAKASH FREIGHT MOVERS LTD. HEAD OFFICE - DELHI, C-130, First Floor, Nareana Industrial Area, Phase - 1, New Delhi - 110 028
Phones : +(91)-(11)-4228222 (20Lines), 011-25897161-7165 Fax : +(91)-(11)-25897167

Quotations valid at least till Jan 31, 2012 should be provided by e-mail/FAX/hardcopy, addressed to

Prof. Jayadeva
C/o Cyber Facilities Laboratory
Electrical Engineering Department
Indian Institute of Technology Delhi
Hauz Khas
New Delhi – 110016
FAX: 91-11-2658 1264
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Quotations must be received by 06-01-2012