

NIQ for RFIC fabrication (RP02473/CSTOR01)

Jan 20, 2012

The committee finalised the following requirements:

1. Si-RFCMOS circuits using 180nm technology have to be fabricated as per design (using UMC L180 PDK) which will be supplied.
2. Four circuits (=blocks), each 1.525mm x 1.525mm will be required. At least 20 pieces of each circuit should be supplied. More pieces (at no extra cost) will be desirable.
3. Pricing for QFN32 packaging should be given as option.

General Terms and conditions:

1. Vendors are requested to send their quotes (technical specs and prices separately) to the office of Dr A Basu, Rm. III-319, C.A.R.E., I.I.T. Delhi-110016, before 5 pm, Feb 7, 2012.
2. Quote should contain terms of payment, and should be valid for at least 90 days.



(Prof G.S. Visweswaran, EE, Chairperson)



(Dr S Chatterjee – EE)



20/1/12

(Dr M. Abegaonkar -CARE)



20-1-12

(Dr A. Basu -CARE)