

**NIQ No. IITD/BCSE/06**

**Due Date 12<sup>th</sup> Dec, 2011**

**Sealed Quotations are invited for Purchase of FPGA Kits with option of Xilinx Spartan 3AN/Altera Cyclone III/ Lattice ECP2 Device and 2 years soft design license of AD Qty 5 Nos.**

Terms and Conditions

1. PAN No. to be submitted with bid.
2. Validity of quotation should be minimum 90 days from the date of submission
3. Sealed quotation must be marked, Head, Deptt of CSE and should reach room No. 404, Bharti Building, IIT Hauz Khas, latest by 12<sup>th</sup> Dec, 2011 upto 5.00 PM Sharp.
4. Quotation must indicate a delivery schedule, which in no case should exceed 4 weeks from the date of placement of order.
5. Quotation must include all taxes and duties, if any.
6. Payment will be made after successful installation as per IIT Norms.
7. Only INR quotes will be accepted.
8. Warranty terms should be clearly mentioned.

(Prof. M. Balakrishnan)