NIQ for Data Acquisition and processing system (MI00900)

Feb 21, 2012

Offers are invited for a data acquisition and processing system with the following features:

- 8-bit (or more), 2 (or more) channel digitizer with at least 2 GS/s real-time sampling rate per channel, capable of handling at least 0 to 0.75 GHz input frequency band.
- At least 256 MB/channel on-board memory for digitizer required. Analog inputs should be through 50 ohm coaxial cables (SMA/SMB preferred).
- PXI Chassis (at least 4 slots), controller (at least 4 GB memory, USB connectivity) and OS (windows as well as a standard RTOS) to process the data from the ADC's.
- Quote for Labview or equivalent tools for processing data, and also a second digitizing card (same specs) to fit into an empty slot, as options.

General Terms and conditions:

- Vendors are requested to send their quotes (technical specs and prices in separate sealed envelopes, both placed in one envelope) to the office of Dr A Basu, Rm. III-319, C.A.R.E., I.I.T. Delhi-110016, before 5 pm, Mar 8, 2012.
- Please ensure that the technical bid includes terms of payment (but NOT prices). Payment after delivery is preferred, but L/C can be arranged.
- 3. Indian agents (including Indian branches of foreign manufacturer) have to include currently valid 'authorized agent' certificate from original manufacturer, and 'import/export code' issued by Finance Ministry.
- Quote should be valid for at least 60 days.
- I.I.T.Delhi reserves the right to accept or reject any/all of the quotations.

Dr A.Basu, CARE

Prof Arun Kumar, CARE

Anaryan Born

Dr M P Abegaonkar, CARE

Prof S D Joshi (EE) - Chairman