RFIC (silicon CMOS-RF) CAD software for design of analog RF circuits up to X-band (and desirably higher frequencies) is required with the following specs:

1. It should be compatible with the libraries provided by UMC and TSMC foundries for 180nm (and desirably 130nm and 65nm) technologies, including layouts.
2. Twenty licenses are required for 3 years.

General Terms and conditions:
1. Vendors are requested to send their quotes (technical specs and prices in separate sealed envelopes, both placed in a sealed envelope) to the office of Dr A Basu, Rm. III-319, C.A.R.E., I.I.T. Delhi-110016, before 5 pm, Nov 15, 2011.
2. Please ensure that the technical bid includes terms of payment (but NOT prices). Payment after delivery is preferred, but L/C can be arranged.
3. Indian agents (including Indian branches of foreign manufacturer) have to include currently valid ‘authorized agent’ certificate from original manufacturer, and ‘import/export code’ issued by Finance Ministry.
4. Quote should be valid for at least 90 days.
5. I.I.T.Delhi reserves the right to accept or reject any/all of the quotations.