

NIQ No. IITD/BCSE/2012-13/02(IRD)

Due Date 08th June, 2012

Sealed Quotations are invited for purchase of upto 3 NOs. FPGA Cards with the following specifications:

1. PCIe Based Card (Gen2 interface)
2. Xilinx Virtex-6 (LX 130T and above)
3. SDRAM : 500MB (optional expandable upto 1GB)
4. Configuration: PCI Bus direct to SelectMAP port and Flash based initialization
5. Drivers for Windows, Linux and VxWorks API with template designs in VHDL and Verilog
6. Optional: Ethernet Connectivity

(Please provide University pricing wherever applicable)

Terms and Conditions

1. Separate Technical and commercial Bids are required to be submitted in separate sealed envelopes otherwise the quotation will be rejected.
2. Compliance Chart should be clearly attached alongwith technical bid.
3. A Valid Agency Ship/Dealership Certificate authorizing the agent to quote to IIT Delhi on behalf of Principals should be enclosed of a latest date in original alongwith the technical bid.
4. Validity of quotation should be minimum 90 days from the date of submission
5. Sealed quotation must be marked, Head, Deptt of CSE and should reach room No. 404, Bharti Building, IIT Hauz Khas, latest by 08th June, 2012 upto 5.00 PM Sharp.
6. Quotation must indicate a delivery schedule, which in no case should exceed 4 weeks from the date of placement of order.
7. Quotation must include all taxes and duties, if any.
8. Payment will be made after successful installation as per IIT Norms.
9. In case of Foreign currency bids, Bids must be made on FOB Basis

Prof. M. Balakrishnan

