

NIQ for RFIC resonator fabrication

Feb 3, 2014

The following are the requirements:

1. An active resonator circuit designed at CARE IIT Delhi has to be fabricated in Si-RFCMOS process using 180nm technology (using UMC L180 PDK). The process must be fully compatible with this PDK.
2. Realization of one designed circuit (=block), 1.525mm x 1.525mm will be required. At least 20 pieces of this circuit should be supplied. More pieces (at no extra cost) will be desirable.

General Terms and conditions:

1. Vendors are requested to send their quotes (technical specs and prices separately) to the office of Dr A Basu, Rm. III-319, C.A.R.E., I.I.T. Delhi-110016, before 5 pm, Feb 18, 2014.
2. Quote should contain terms of payment, and should be valid for at least 60 days.