

INDIAN INSTITUTE OF TECHNOLOGY

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Subject: sealed quotations are invited for Xeon Tower Workstation with support for 2 Xeon Phi coprocessor

DUE DATE: - 1<sup>st</sup> January 2014, till 11am

QUANTITY: - One

Sealed quotations are invited from reputed manufacturer/bidder for purchase of Xeon Tower Workstation with support for 2 Xeon Phi coprocessor in the department. There will be a two bid system-a technical bid & a financial bid. Technical bid should not contain any commercial information, else will be rejected. The financial bid will be opened only if technical bid functionally meet the tender specification. Before submitting the tender, read the tender terms & conditions carefully.

**Technical Specification:**

**Xeon Tower workstation with support for 2 Xeon Phi coprocessors**

| Parameter           | Specification  |
|---------------------|--|
| Model name          | Make and model to be quoted by the bidder  |
| CPU                 | Dual Intel Xeon E5-2680, 2.7 GHz, 20 MB cache, 8 GT/s QPI, support (physical space and slots) for 2 Xeon Phi coprocessor |
| No. of CPU          | Machine to be supplied with 2 processors   |
| Cores per processor | 8 or more  |
| Chipset             | Suitable Intel C602 or higher  |
| Memory requirement  | The system should be supplied with at least 128GB memory and should have support for expansion to up to 256GB.           |
| HDD Requirement     | The system should be supplied with 6Gb/s SAS or SATA, at least 7200RPM, HDD with total capacity of at least 6TB.         |
| Network             | At least 2 gigabit Ethernet port   |
| I/O expansion slots | 4x PCI-E 3.0 x16 (double-width)<br>2x PCI-E 3.0 x8 (1 in x16 slot)<br>1x PCI-E 2.0 x4 (in x8)                            |
| USB                 | At least 4 USB ports to be supported   |
| Video               | 1 VGA port   |
| Form factor         | Tower workstation  |
| Power supply        | High efficiency 75% redundant power supply   |
| Support             | 3 Years comprehensive 24x7 support.  |
| OS certification    | Windows 2012 enterprise  |

**Terms & Condition:**

Separate Technical & Commercial bids are required to be submitted in separate sealed envelopes. Technical details with commercial information are bound to be rejected.

If the bidder is an authorized dealer of any manufacturer, the authorized Indian dealership certificate from the Principals should be enclosed specific to this tender at a recent date in original.

Either the Indian agent on behalf of the Principal/OEM or Principal/OEM itself can bid but both cannot bid simultaneously for the same item in the same tender. If an agent submits bid on behalf of the Principal/OEM, the same agent shall not submit a bid on behalf of another Principal/OEM in the same tender for the same item.

IIT Delhi is exempted from paying Excise Duty & necessary Excise Duty Exemption Certificate will be provided.

Three year's comprehensive warranty be provided.

Payment will be made after installation as per IIT Delhi norms.

**Delivery period:** within 4 weeks from the issue of supply order or specify the delivery period.

The quotation must have validity of three months from the date of submission.

The item will be used for educational purpose; any applicable academic institution discount should be offered & stated clearly.

IIT Delhi reserves the right to reject any or all quotations without assigning any reasons.

No query regarding the tender will be entertained.

The Bidder Company must be ISO Certified. (Certificate copy to be attached with Technical bid).

Only INR Bids will be accepted. (No CDEC on INR Bid will be issued by the Institute)

Quotation must include the applicable taxes and duties, if any.

Post warranty AMC in %

Compliance chart should be attached along with technical bid and must match with the technical data sheet.

Sealed Quotations must be marked "Quotation for Xeon Tower Workstation with support for 2 Xeon Phi coprocessor

and should reach latest by 01/01/14 up to 11.00 am at the following address

The Head  
Department of Computer Science & Engineering,  
Bharati Bldg. 3<sup>rd</sup> floor, Room No 404,  
IIT Delhi-110016.

Dr.Ragesh Jaiswal