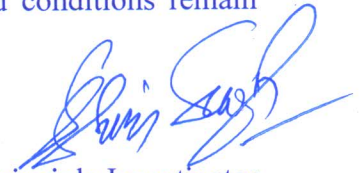


Indian Institute of Technology, Delhi
Electrical Engineering Department

Corrigendum for Purchase of Four Three Level Voltage Source Inverters
(NIT number 3810)

Date 2-12-2013

The last date to submit technical and financial bid for NIT number 3810 have been extended to 10-12-2013. The bids will be opened on 12-12-2013. All other terms and conditions remain unchanged.



Principle Investigator

(Prof. Bhim Singh)