INDIAN INSTITUTE OF TECHNOLOGY, DELHI Department of Mechanical Engineering

July 17, 2013

Notice Inviting Quotation

Sealed technical and commercial quotations are invited for purchase of **Embedded Electronic Kits and Software** as per below mentioned specifications:

S.No.	Items		Qty
1.	ARM 7	Microcontroller Development kit with following	10 nos.
	specificat	ions:	
	i)	72 MHz STM32F103RB ARM Cortex TM -M3 processor-	
		based MCU in 64-pin LQFP	
	ii)	On-Chip Memory: 128KB Flash & 20KB RAM	
	iii)	USB 2.0 Full Speed Device Interface	
	iv)	CAN and UART interfaces	
	v)	I2C, SPI and GPIO via PCB headers	
	vi)	Character LCD (16x2)	
	vii)	Analog Voltage Control for ADC Input	
	viii)	8 LEDs and 3 push-buttons	
	ix)	Power via USB connector	
	X)	Debug Interface Connectors	
	xi)	20-pin ARM Standard JTAG (0.1 inch connector)	
	xii)	With JTAG debugger for ARM Cores	
2.	ARM 7	Microcontroller Development kit with following	5 nos.
	specificat	ions:	
	i)	72MHz STM32F107VC ARM Cortex [™] -M3 processor-	
	••	based MCU in 100-pin LQFP	
	11)	On-Chip Memory: 256KB Flash & 64KB RAM	
	111)	External Memory: 8KB I2C Flash	
	1V)	Color QVGA TFT LCD with touch screen	
	v)	10/100 Ethernet Port	
	V1)	USB 2.0 Full Speed - USB, USB-OTG, & USB Host	
	V11)	2 CAN Interfaces	
	V111)	Serial/UART Port	
	1X)	Micro SD Card Interface	
	X)	5-position Joystick and push-button	
	X1)	3-axis Motion sensor / Accelerometer	
	XII) viii)	Audio CODEC with Line In/Out and Speaker	
	\mathbf{x} \mathbf{x} \mathbf{y}	SO GDIO pins	
	XIV)	Debug Interface Connectors	
		20 pin ITAG (0.1 inch connector)	
	xvii)	10 pin Cortex debug (0.05 inch connector)	
	xviii)	20-nin Cortex debug \pm ETM Trace (0.05 inch connector)	
	<pre>iii) iv) v) v) vi) vii) viii) ix) xi) xii) xi</pre>	External Memory: 8KB I2C Flash Color QVGA TFT LCD with touch screen 10/100 Ethernet Port USB 2.0 Full Speed - USB, USB-OTG, & USB Host 2 CAN Interfaces Serial/UART Port Micro SD Card Interface 5-position Joystick and push-button 3-axis Motion sensor / Accelerometer Analog Voltage Control for ADC Input Audio CODEC with Line-In/Out and Speaker 80 GPIO pins Debug Interface Connectors 20 pin JTAG (0.1 inch connector) 10 pin Cortex debug (0.05 inch connector) 20-pin Cortex debug + ETM Trace (0.05 inch connector)	

	xix)	With JTAG debugger for ARM Cores,	
3.	Altium D	esigner 10 Academic License Upgrade from R10 to R13	1 Set
	with 1 yea	ar support	
4.	DRV8412	evaluation kit (DRV8412-C2-KIT) includes everything	2 Nos.
	needed to	control two brushed DC or a single stepper motor	
	1)	the DRV8412 motor driver, a C2000 Biasala E28025 MCU controlCADD	
	11 <i>)</i> ;;;;)	a C2000 Piccolo F28035 MCU controlCARD,	
	111)	environment and motors	
5	ARM9	Microcontroller Development kit with following	2 Nos
5.	specificat	ions:	21105.
	i)	125MHz LPC2929 ARM968E-S processor-based MCU in	
	,	144-pin LQFP	
	ii)	On-Chip Memory: 768KB Flash, 56KB RAM, & 16KB	
		EEPROM	
	iii)	External Memory: 1MB SRAM	
	iv)	Color QVGA TFT LCD with touchscreen	
	v)	USB 2.0 Full Speed - USB, USB-OTG, & USB Host	
	V1)	2 CAN Interfaces	
	V11)	Serial Ports	
	V111)	MicroSD Card Interface	
	IX) v)	Analog Voltage Control (2) for ADC Input	
	xi)	104 GPIO pins	
	xii)	20 pin ITAG (0.1 inch connector)	
	xiii)	JTag Debugger	
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6.	ARM9	Microcontroller Development kit with following	2 Nos.
	specificat	ions:	
	i)	MCU STR912FAW44X	
	ii)	XTAL 25 MHz	
	111)	ARM Processor ARM966E-S	
	1V)	On-Chip RAM 96K	
	\mathbf{v}	Dush Puttons 2	
	vi) vii)	I/O Port I EDs 8	
	viii)	Analog Input (Potentiometer)	
	ix)	Serial Ports 2	
	x)	CAN Ports 1	
	xi)	USB Device Interface	
	xii)	Ethernet Interface 10/100	
	xiii)	SD Card Interface	
	xiv)	LCD Character	
	xv)	Debug Interface	
	xvi)	JTAG Interface	
	XV11)	20-pin JTAG Connector	

Terms and Conditions:

- 1. Commissioning support at IIT Delhi will be provided along with the system.
- 2. Payment terms: As per IIT norms.
- 3. Technical compliance chart should be strictly attached with the bid.
- 4. Warranty should be 1 years.
- 5. All supplies should be complete within 8 weeks.
- 6. Quotations not sent in separate covers for technical and commercial bids are bound to be rejected.
- 7. Quotation validity should be minimum 60 days.
- 8. IIT Delhi reserves the right to accept or rejects any quotation without assigning any reasons thereof.

The quotations may reach the following on or before 31st July 2013 up to 5.00PM.

Dr. Sunil Jha Associate Professor Department of Mechanical engineering Room No. 156, Block III IIT, Delhi, Hauz Khas, 110016, New Delhi