

**NIQ for RFIC fabrication (RP02473/CSTOR01)**

Apr 16, 2013

The requirements are:

1. Si-RFCMOS circuits using 130nm technology have to be fabricated as per design (using UMC L130 PDK) which will be supplied.
2. 1 circuit (=blocks), 1.525mm x 1.525mm will be required. At least 20 pieces of each circuit should be supplied. More pieces (at no extra cost) will be desirable.

General Terms and conditions:

1. Vendors are requested to send their quotes (technical specs and prices separately) to the office of Dr A Basu, Rm. III-319, C.A.R.E., I.I.T. Delhi-110016, before 5 pm, Apr 30, 2013.
2. Quote should contain terms of payment, and should be valid for at least 60 days.