

**Centre for Applied Research in Electronics
Indian Institute of Technology, Delhi**

Dated: 26th December 2012

Notice Inviting Quotations for Multi-Processor DSP Kit

Due Date: 15th January 2013

Sealed quotations are invited for the supply of one Multi-Processor DSP Kit with Data Acquisition. The technical and commercial bids must be enclosed in separately sealed envelopes, marking as "Technical bid" and "Commercial bid" on the respective covers. Both the envelopes should be put in a common sealed envelope. The required specifications are as follows:

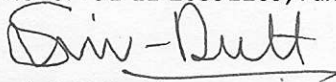
1. **Stand-alone Base System:** Consisting of i) dual 500 MHz Texas Instruments C6472 DSPs, ii) Virtex-5 FPGA, iii) USB 2.0 slot, iv) microSD card slot, v) RS232 connectivity, vi) Power supply, vii) 64 MB flash memory.
2. **Data Acquisition Module:** i) 4 Analog-to-Digital Converters, 16-bit resolution at 600 KSPS, based on Texas Instruments ADS8372 chip, ii) 4 Digital-to-Analog Converters, 16-bit resolution at 2.7 MSPS, based on Texas Instruments DAC8831 chip.
3. **XDS560Plus USB JTAG Emulator**
4. **Stand-alone Module Carrier:** Consisting of i) Module site, ii) UART interface, iii) RS232 interface, iv) JTAG, v) 12V supply, v) USB2.

Notes:

1. The quotation must include FOB and CIF for delivery to the Indian Institute of Technology, Delhi, India.
2. The quotation must have a validity of at least three months.
3. The system should be covered with 1 year of warranty.
4. All standard software, drivers and accessories for the system should be provided.
5. Technical compliance with the required specifications may be given for each item.
6. Supplier must enclose photocopy of PAN/TIN of the company.
7. In case this is a proprietary item, a proprietary item certificate should be included from the manufacturer or sole authorized agent/distributor/re-seller. In the latter case, a certificate of sole agent/distributor/re-seller authorization from the manufacturer should also be included.
8. The Purchase Committee reserves the right to accept or reject any or all of the quotations without assigning any reasons.

The quotations must be submitted by 5:00 pm on 15th January 2013 to the following:

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Indian Institute of Technology, Delhi
Hauz Khas
New Delhi – 110016, INDIA.
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**Prof. S. D. Joshi
(Purchase Committee Chairman)**