NIQ for RFIC fabrication

Dec 17, 2012

The committee finalised the following requirements:

1. Si-RFCMOS circuits using 180nm, 130nm technology have to be fabricated as per design (using UMC L180 and L130 PDK) which will be supplied.
2. Two circuits (=blocks), each 1.525mm x 1.525mm (one for 180nm and one for 130 nm) will be required. At least 20 pieces of each circuit should be supplied. More pieces (at no extra cost) will be desirable.

General Terms and conditions:

1. Vendors are requested to send their quotes (technical specs and prices separately) to the office of Dr A Basu, Rm. III-319, C.A.R.E., I.I.T. Delhi-110016, before 5 pm, Jan 1, 2013.
2. Quote should contain terms of payment, and should be valid for at least 90 days.